# UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO. : 6,784,099 B1 DATED : August 31, 2004 Page 1 of 2

INVENTOR(S) : Naem

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

## Column 1,

Line 46, delete "Is" and replace with -- is --. Line 47, delete "It" and replace with -- it --.

## Column 3,

Line 64, delete "polslilcon" and replace with -- polysilicon --.

#### Column 4,

Line 39, delete "Is" and replace with -- is --.

# Column 5,

Lines 8 and 44, delete "In" and replace with -- in --. Line 52, delete "Introduced" and replace with -- introduced --.

#### Column 6,

Line 5, delete "polyslilcon" and replace with -- polysilicon --.

Line 9, delete "In" and replace with -- in --.

Line 12, delete "Is" and replace with -- is --.

Line 21, delete "Include" and replace with -- include --.

Line 44, delete "Is" and replace with -- is --.

# Column 7,

Line 19, delete "Integrated" and replace with -- integrated --.

Line 39, after "wafer" insert -- a --.

Line 45, before "form" insert -- to --.

Line 59, delete "2" and replace with -- 3 --.

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Page 2 of 2

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 9,

Line 47, delete "laver" and replace with -- layer --.

Signed and Sealed this

Seventh Day of December, 2004

JON W. DUDAS Director of the United States Patent and Trademark Office